

MENU

SEARCH

INDEX

DETAIL

JAPANESE

BACK

NEXT

7 / 18

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2000-194655
 (43)Date of publication of application : 14.07.2000

(51)Int.Cl. G06F 13/38
 G06F 13/00
 H04L 12/56
 H04L 13/08

(21)Application number : 11-277881 (71)Applicant : STMICROELECTRONICS INC
 (22)Date of filing : 30.09.1999 (72)Inventor : CHRISTIAN D CASPER

(30)Priority

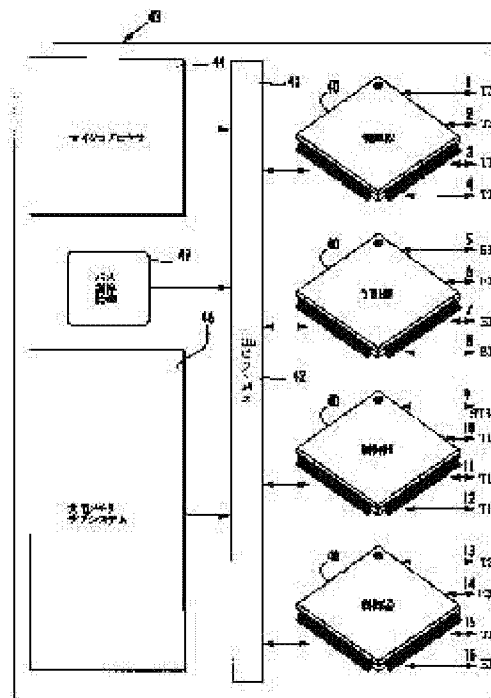
Priority number : 98 163952 Priority date : 30.09.1998 Priority country : US

(54) METHOD AND SYSTEM FOR TRANSFER CONTROL OF DATA BY UPDATING DESCRIPTOR IN DESCRIPTOR RING

(57)Abstract:

PROBLEM TO BE SOLVED: To reduce the overhead regarding the use of descriptors and descriptor rings relating to respective frame data buffers by updating only the starting and ending descriptors in a descriptor chain for a desired host or controller.

SOLUTION: A network controller 40 updates a starting and an ending descriptor entry which are concerned to optimize the use of a bus when ≥ 3 frame data buffers are chained together. When the network controller 40 completes the process as to a buffer relating to chained frames, the possession of the ending descriptor is returned first and then the possession of the starting descriptor is returned. The flags and fields of the starting and ending descriptors in a column type chain of a fence post, i.e., a fence are all updated by the controller 40 and when they are completely sent or received, accurate information regarding the frames is provided.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]